



**INDIAN INSTITUTE OF TECHNOLOGY PATNA
BIHTA PATNA-801106
RESEARCH & DEVELOPMENT UNIT**

ADVT NO: R&D/547/GITA-DST/198

DATED: 02.07.2021

Project No. 547

Applications are invited in the prescribed format only for the following assignment in a purely time bound research project undertaken in this institute.

1. (a) Name of the temporary assignment : Junior Research Fellow
(b) Number of Post : 1(One)
(c) Duration of the Post : 3 Years
2. Name of the temporary research project : “Design, Fabrication and Modeling of Novel GAA FETs”.
3. Name of the sponsoring Agency : GITA/DST
4. Fellowship : Rs. 31,000/- plus HRA as per GoI rule.
5. Qualifications & Experience
 - (i) For candidates with ME/M.Tech/MS degree in Electronics/Electrical Engineering/ Microelectronics/Nanoelectronics/VLSI Design/Solid State/ other related specialization, first class (minimum 60% marks or 6.5 CPI) in M.Tech./ME/MS with GATE qualifications
 - (ii) For candidates with BE/B.Tech in Electronics/Electrical/ Electronics and Communication/or related, 70% marks or 7.5 CPI in B.Tech./BE with valid GATE Score
 - (iii) The age should not exceed 28 years for a candidate with BE/B.Tech. degree as the highest qualification and 32 years for a candidate with ME/M.Tech/MS degree as the highest qualification.
 - (iv) Relaxations for SC/ST/OBC/women/PD will be given as per the GoI rules.
 - (v) Those candidates who have Experience of Device Simulations using TCAD software or Device Characterization using parameter analyser/probe station etc will be given preference
 - (vi) The selected candidate may be encouraged to join the Ph.D program of IIT Patna by following the procedure of Ph.D selection process if he/she performs well in the project work

Candidates interested in this position and satisfying the qualification criteria should write an email to the project investigator **Dr. Pramod Kumar Tiwari**, Department of Electrical Engineering, IIT Patna (Email IDs: pktiwari@iitp.ac.in, pramod23amu@gmail.com). The subject of the email should read as “**JRF Position DST-547**”. The last date for receiving this email is **20th July 2021**.



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The email MUST include the scanned/pdf copy of duly filled application form (see below) with applicant's signature. The email MUST include self-attested scanned pdf copy of all supporting documents (degree certificates, mark-sheets, GATE scorecard, and category certificate, if applicable). The application should also include a brief Academic CV not exceeding two page.

IIT Patna reserves the right to not shortlist any candidate in case the application email does not contain complete information backed up by supporting documents as listed above. All candidates who apply via email by 20th July 2021 (deadline) and are shortlisted will be informed regarding the further details by 25th July 2021. **Date of ONLINE Interview: 30th July 2021.** It is the responsibility of the applicant to ensure that they have reliable internet connectivity on the date of online interview.

Deputy Registrar

Copy to:

1. Associate Dean, R&D, IIT Patna
2. Advertisement file
3. Project file



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PROJECT CODE: R&D/SP/EE/DST/2021-22/547

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FORMAT OF APPLICATION FOR “JUNIOR RESEARCH FELLOW”

Name & Address Including email id and Phone no. (for Correspondence)	Category (GEN/OBC/SC/S T/PD)	DOB dd/ mm/yy	Professional Exam. (GATE/CSIR-NET, etc) & Validity
NAME IN CAPITAL Address: Phone: Email:			
Educational Qualification			
Institute/ Board	Exam Passed	Year of Passing	% of Marks/CPI
	10 th Class		
	12 th Class		
	Bachelors (B.Sc/B.Tech/B.E./BCA) or equivalent		
	Masters (M.Sc/M.Tech/M.E/MCA/ MA) or equivalent		

Qualifying degree	Degree/ major/Specialization
(B.Sc/B.Tech/B.E./BCA)	
(M.Sc/M.Tech/M.E/MA/MCA)	
Others	

Signature of applicant

Date:
Place: